

PATENT NUMBER and ISSUE DATE

U.S. UTILITY Patent Application

***CONTINUING DATA VERIFIED: THIS APPLIN CLAIMS BENEFIT OF 60/263,641 01/23/2001 ***FOREIGN APPLICATIONS VERIFIED: PG-PUB DO NOT PUBLISH		APPL NUM 10056317	FILING DATE 01/23/2002	CLASS	SUBCLASS 420	GAU 267/ 2105	EXAMINER - Zanas					
** FOREIGN APPLICATIONS VERIFIED: PG-PUB DO NOT PUBLISH RESCIND ATTORNEY DOCKET NO 35 USC 119 conditions met yes 2 no Verified and Acknowledged Examiners's initials TITLE: Method and apparatus for implementing spread memory layout		**APPLICANTS: Knittel Guenter;										
** FOREIGN APPLICATIONS VERIFIED: PG-PUB DO NOT PUBLISH Poreign priority claimed per priorit												
PG-PUB DO NOT PUBLISH RESCIND Foreign priority claimed yes no ATTORNEY DOCKET NO 35 USC 119 conditions met yes no Verified and Acknowledged Examiners's initials 10011617 -2 TITLE: Method and apparatus for implementing spread memory layout												
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Foreign priority claimed		** FOREIGN APPLICATIONS VERIFIED:										
35 USC 119 conditions met □ yes □ no Verified and Acknowledged Examiners's initials 10011617 -2 TITLE: Method and apparatus for implementing spread memory layout	-				RESCINI) [
TITLE: Method and apparatus for implementing spread memory layout U.S.DEPT. OF COMM./PAT.& TM-PTO-436L/Rev. 12-94	3 V	5 USC 119 condition erified and Acknow	ns met ledged Examiners's inti	□ yes { als	⊐ no		10011617 -2					
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NOTICE OF ALL	OWANCE MAILED	·	CLAIMS ALLOWED				
		Assistant Examiner	Total Claims		Print Claim for O.G		
ISS	UE FEE			DRAWING			
Amount Due	Date Pald	7	Sheets Drwg.	Figs.Drw	g. Print Fig.		
	<u> </u>	Primary Examiner					
TE	RMINAL	PREPARED FOR ISSUE	Application Examiner				
	DISCLAMER	WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.					
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